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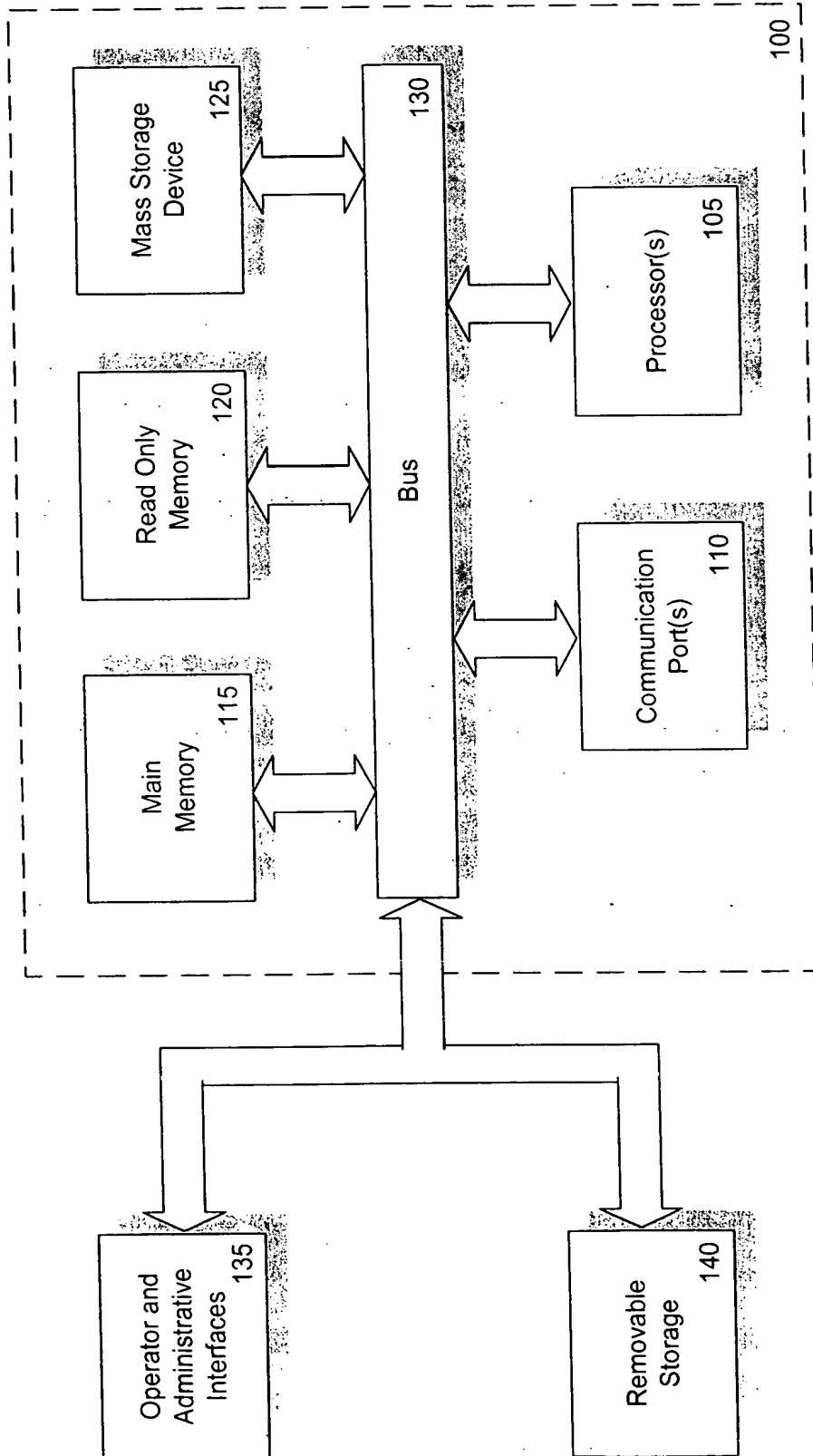


Figure 1

| | SGPOS | C ² E ² ₁ | C ² E ² ₂ |
|--|----------|--|--|
| Processors | X | X | X |
| Banks/ranges of Memory | X | X | X |
| LAN Adapter Cards | | X | |
| Peripheral Devices and Associated Adapter Cards | X | | X |
| Logical Resources (e.g., protection IDs) | X | X | X |

Figure 2A

| | SGPOS | C ² E ² ₁ | C ² E ² ₂ |
|--|----------|--|--|
| Processors | | X | X |
| Banks/ranges of Memory | X | X | X |
| LAN Adapter Cards | | X | |
| Peripheral Devices and Associated Adapter Cards | | | X |
| Logical Resources (e.g., protection IDs) | | X | X |

Figure 2B

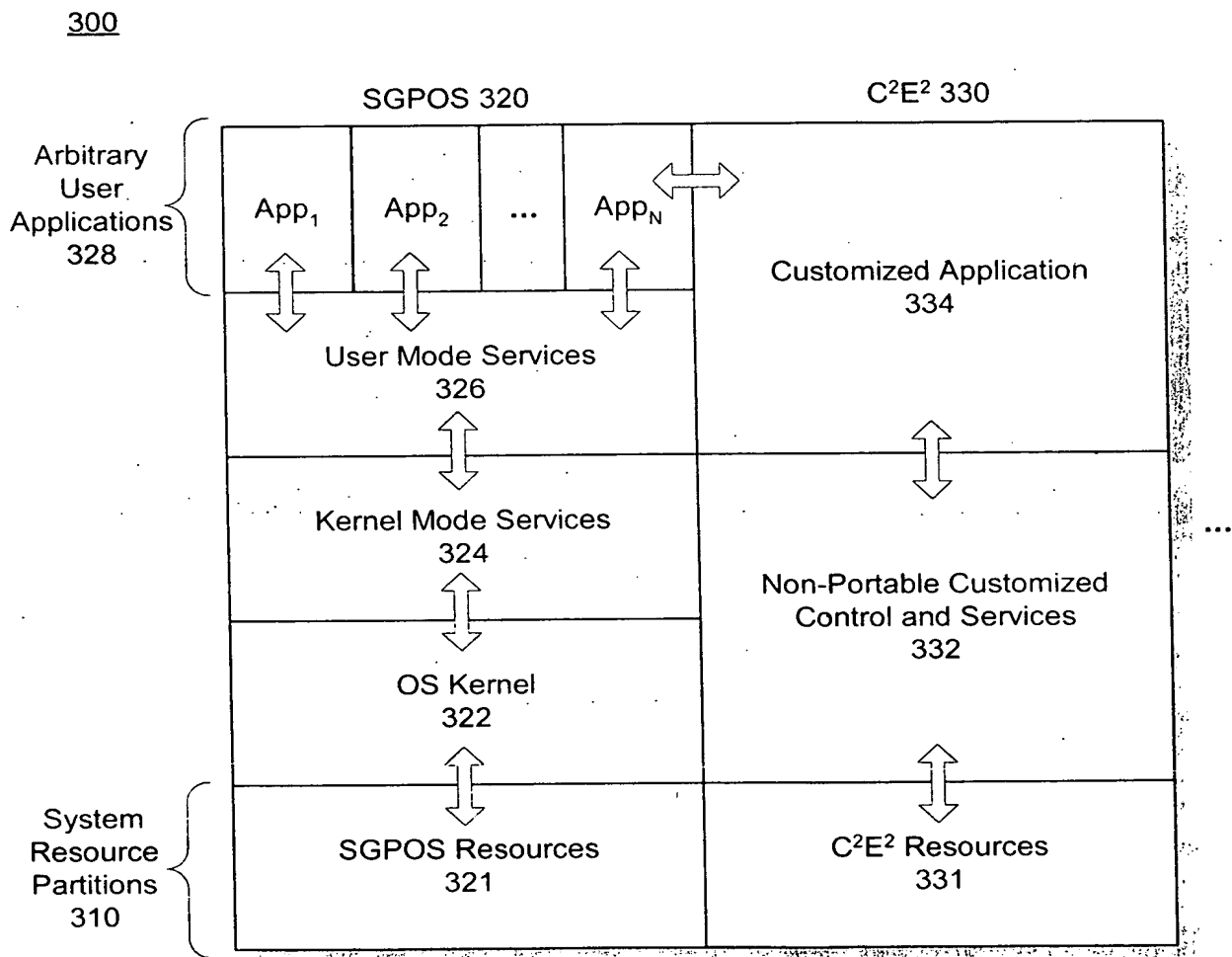


Figure 3

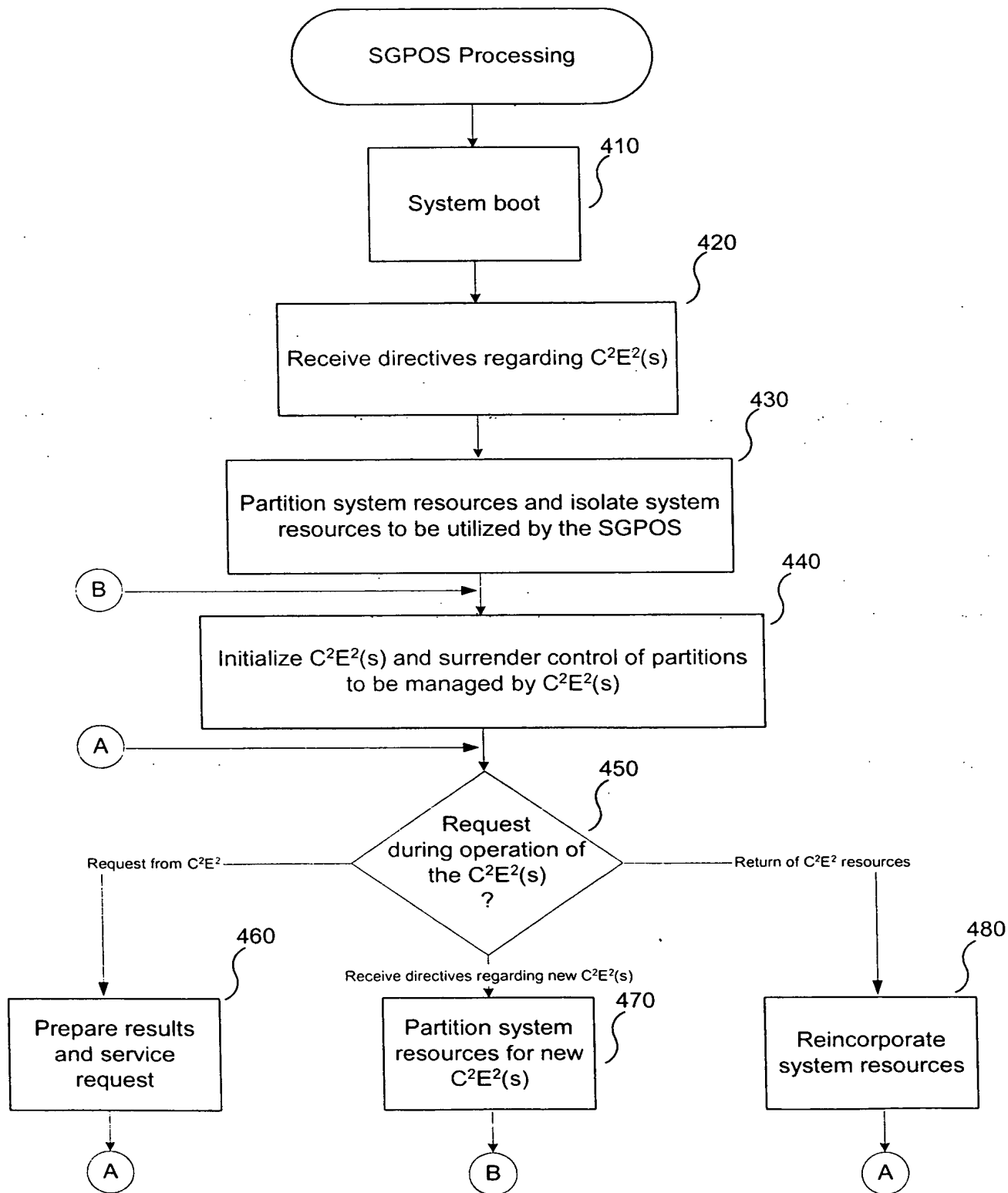


Figure 4

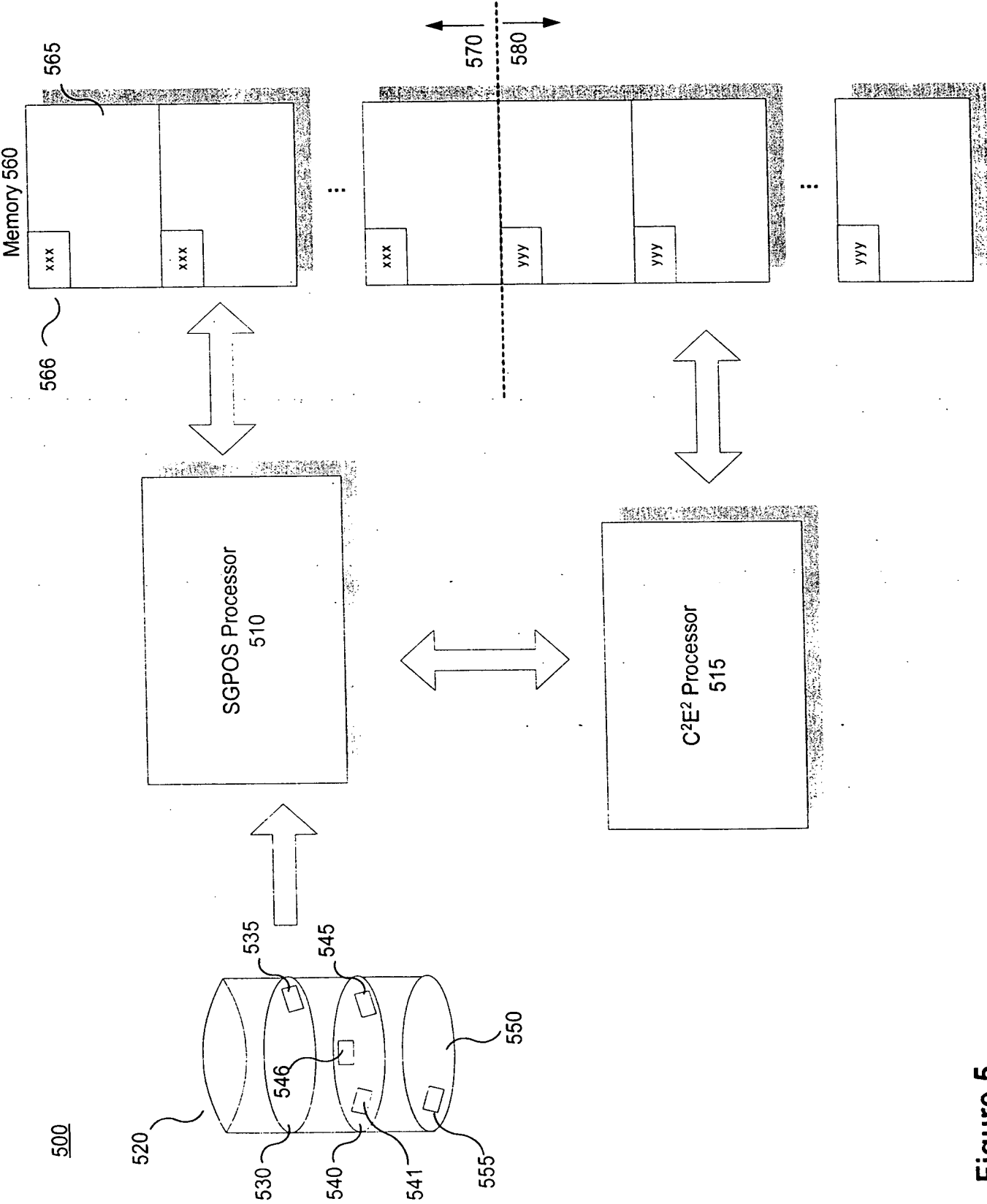


Figure 5

600

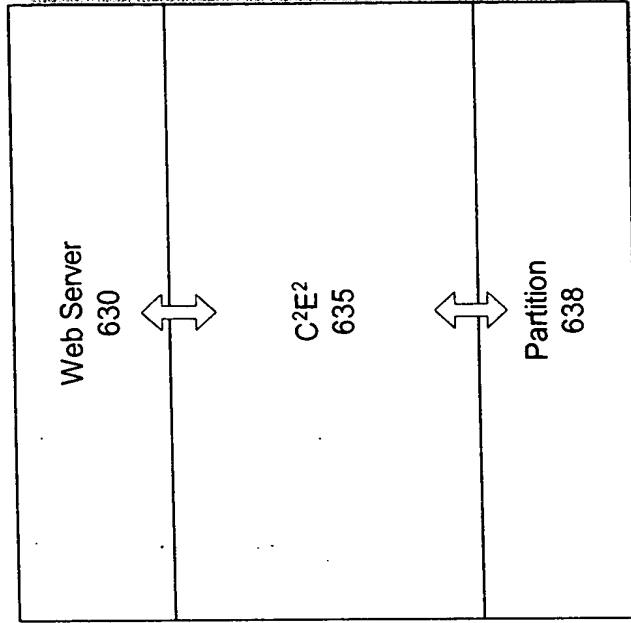


Figure 6B

600

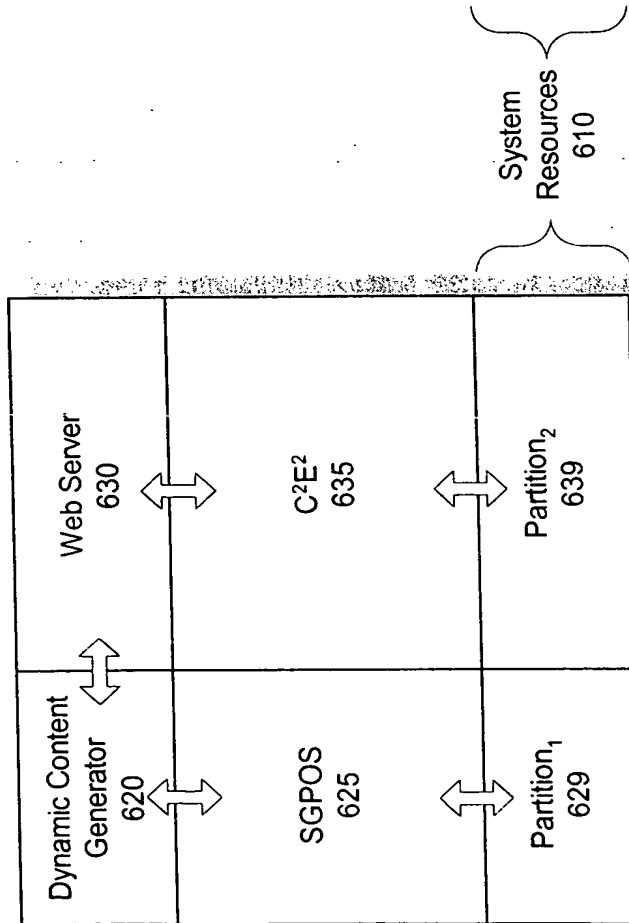


Figure 6A